

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6164	abe-n\$.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/28 14:14
L2	105	1 and modulat\$3 adj signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/28 14:14
L3	46	2 and transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/28 14:14
L4	86416	modulat\$3 adj signal	US-PGPUB; USPAT	OR	ON	2008/04/28 14:19
L5	306593	transistor with parallel signal adj2 (line wir\$4)	US-PGPUB; USPAT	OR	ON	2008/04/28 14:20
L6	1286	transistor with parallel with signal adj2 (line wir\$4)	US-PGPUB; USPAT	OR	ON	2008/04/28 14:20
L7	19	4 and 6	US-PGPUB; USPAT	OR	ON	2008/04/28 14:20
L8	4216	transistor same parallel same signal adj2 (line wir\$4)	US-PGPUB; USPAT	OR	ON	2008/04/28 14:22
L9	127	4 and 8	US-PGPUB; USPAT	OR	ON	2008/04/28 14:22
L10	96	9 and @ad<="19990611"	US-PGPUB; USPAT	OR	ON	2008/04/28 14:22
L11	5	10 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/28 14:22
L12	0	((modulat\$3 adj signal) and transistor with parallel and overlap).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 14:31
L13	0	((modulat\$3 adj signal) and transistor same time adj period same overlap).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 14:33
L14	0	(display and transistor same time adj period same overlap).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 14:33
L15	11	(display and transistor and time adj period same overlap).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 14:34

L16	184	((modulat\$3 adj signal) and transistor with parallel).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 15:37
L17	0	((modulat\$3 adj signal) and display and driving adj circuit with transistor with parallel).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 15:38
L18	7	((modulat\$3 adj signal) and driv\$3 adj circuit same transistor with parallel).clm.	US-PGPUB; USPAT	OR	ON	2008/04/28 15:38

4/ 28/ 08 3:39:40 PM

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